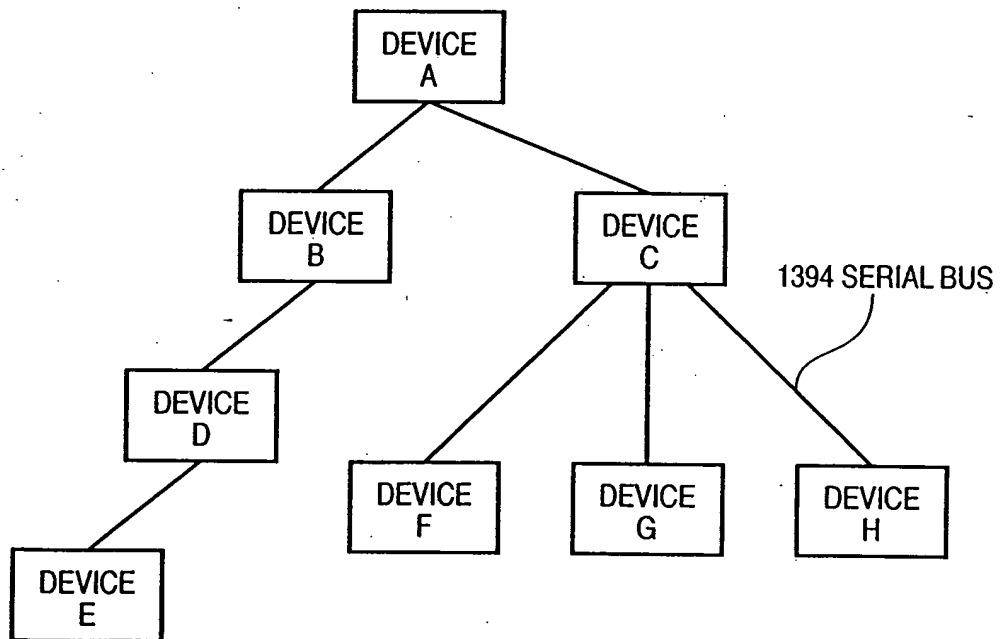
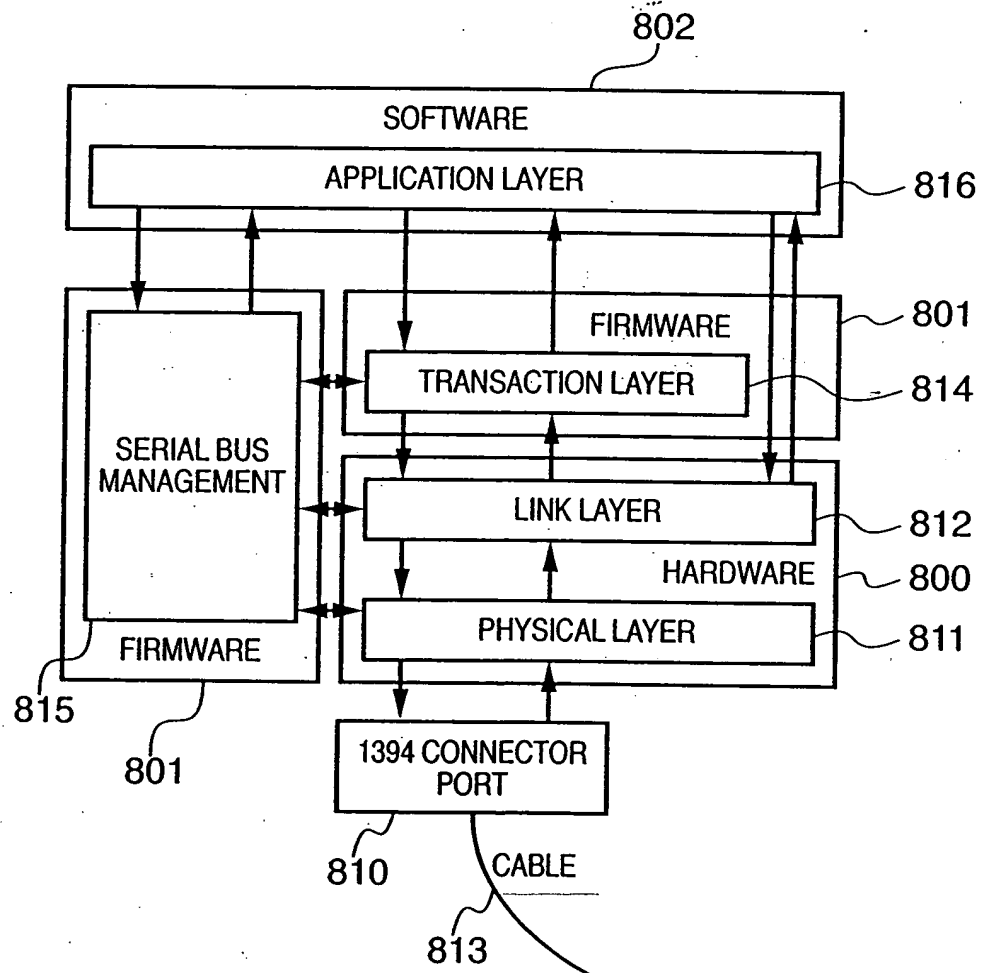


FIG. 1



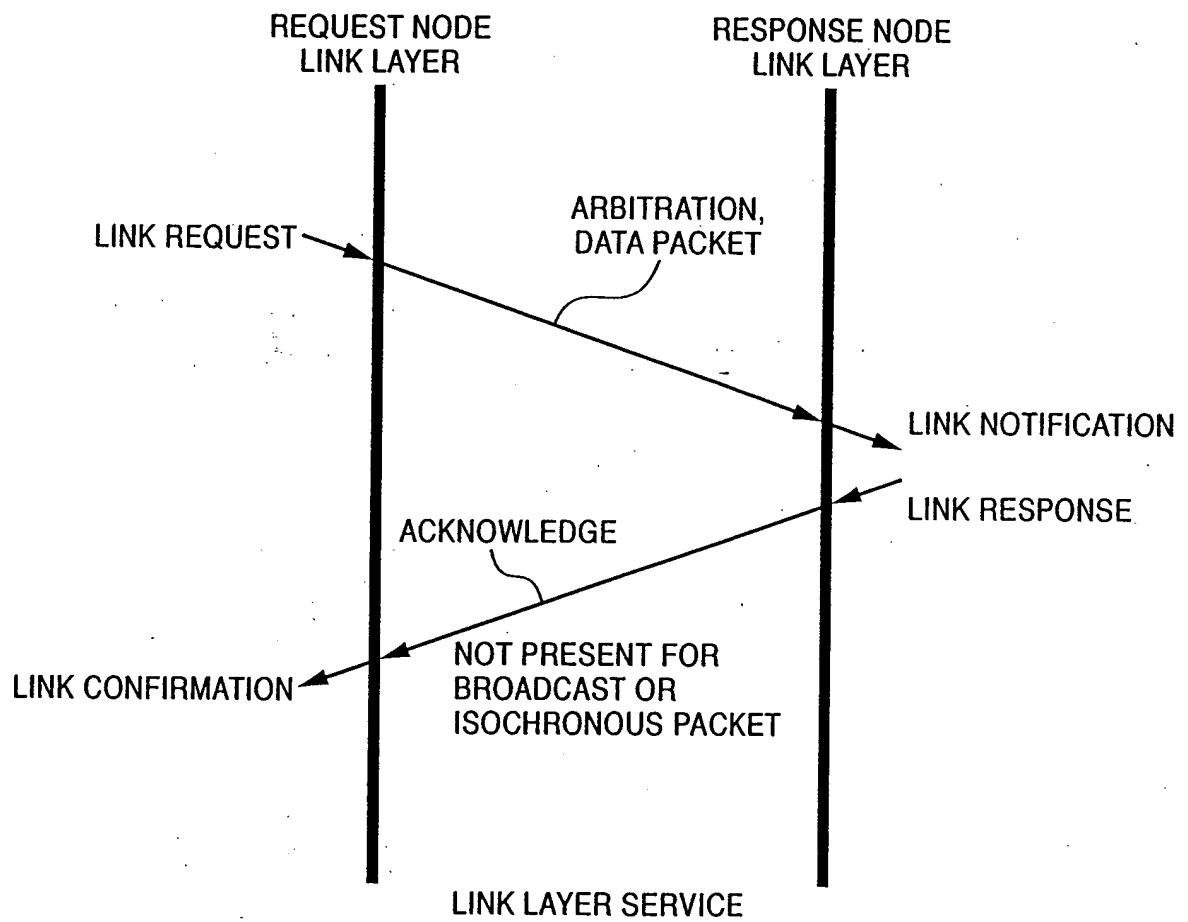
00939806-082801

FIG. 2



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FIG. 3



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FIG. 4

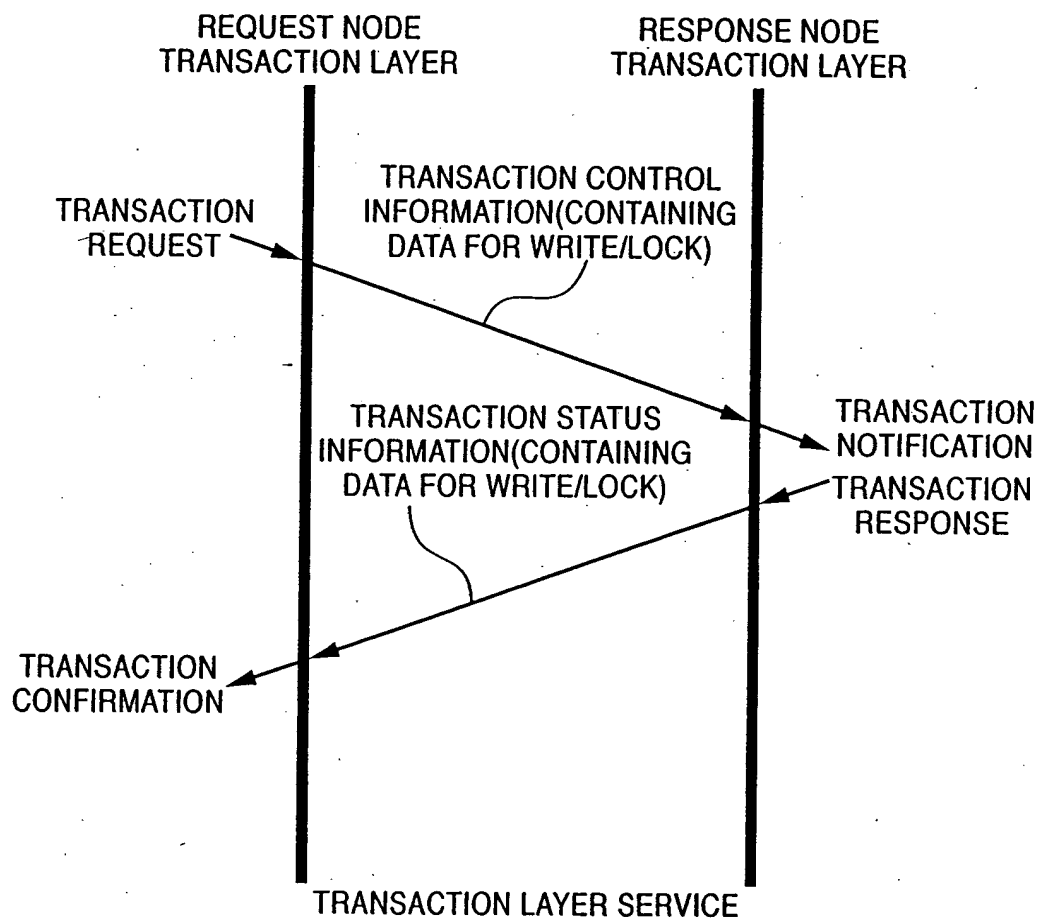


FIG. 5

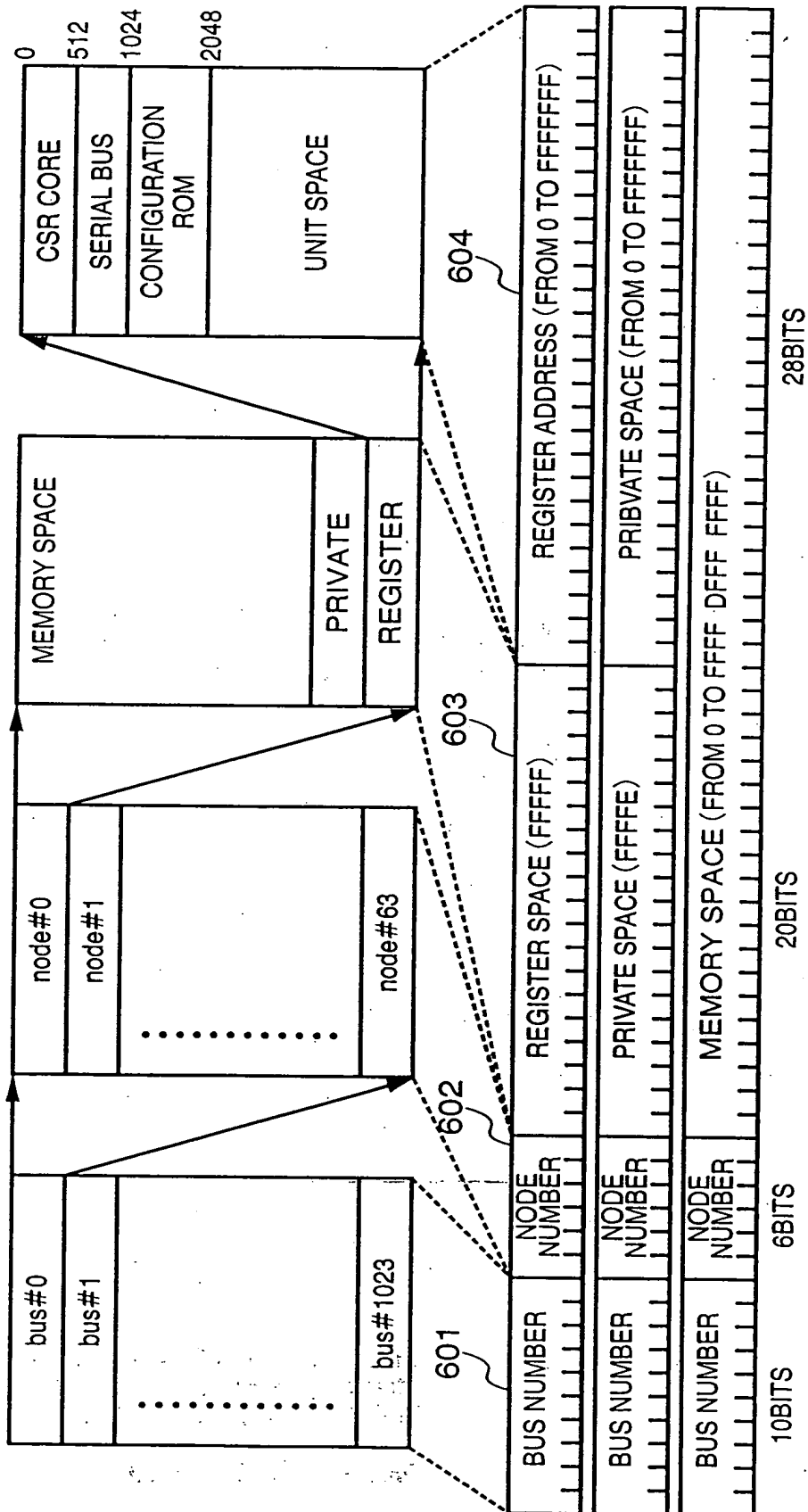


FIG. 6

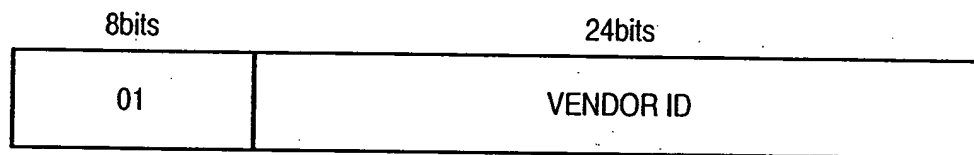
CSR CORE REGISTER		
OFFSET (HEXADECIMAL)	REGISTER NAME	FUNCTION
000	STATE_CLEAR	STATE AND CONTROL INFORMATION
004	STATE_SET	INFORMATION REPRESENTING STATE_CLEAR WRITE
008	NODE_IDS	BUS ID + NODE ID
00C	RESET_START	BUS IS RESET BY WRITE IN THIS AREA
010~014	INDIRECT_ADDRESS, INDIRECT_DATA	REGISTER FOR ACCESSING FOR LARGER THAN 1K
018~01C	SPLIT_TIMEOUT	VALUE OF TIMER FOR DETECTING TIMEOUT OF SPLIT TRANSACTION
020~02C	ARGUMENT, TEST_START, TEST_STATUS	REGISTER FOR DIAGNOSIS
030~04C	UNITS_BASE, UNITS_BOUND, MEMORY_BASE, MEMORY_BOUND	NOT PREPARED FOR IEEE1394
050~054	INTERRUPT_TARGET, INTERRUPT_MASK	INTERRUPT NOTIFICATION REGISTER
058~07C	CLOCK_VALUE, CLOCK_TICK_PERIOD, CLOCK_STOROB_E_ARRIVED, CLOCK_INFO	NOT PREPARED FOR IEEE1394
080~0FC	MESSAGE_REQUEST, MESSAGE_RESPONSE	MESSAGE NOTIFICATION REGISTER
100~17C		RESERVATION
180~1FC	ERROR_LOG_BUFFER	RESERVATION FOR IEEE1394

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FIG. 7

SERIAL BUS REGISTER		
OFFSET (HEXADECIMAL)	REGISTER NAME	FUNCTION
200	CYCLE_TIME	COUNTER FOR ISOCHRONOUS TRANSFER
204	BUS_TIME	REGISTER FOR TIME SYNCHRONIZATION
208	POWER_FAIL_IMMINENT	REGISTER RELATED TO POWER SUPPLY
20C	POWER_SOURCE	
210	BUSY_TIMEOUT	CONTROLLING RETRY OF TRANSACTION LAYER
214 }		RESERVATION
218		
21C	BUS_MANAGER_ID	NODE ID OF BUS MANAGER
220	BANDWIDTH_AVAILABLE	MANAGING BAND FOR ISOCHRONOUS TRANSFER
224 }	CHANNELS_AVAILABLE	MANAGING CHANNEL NUMBER OF ISOCHRONOUS TRANSFER
228		
22C	MAINT_CONTROL	REGISTER FOR DIAGNOSIS
230	MAINT_UTILITY	
234 }		RESERVATION
3FC		

00000000-00000000

FIG. 8

CONFIGURATION ROM WITH MINIMAL FORMAT

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FIG. 9

Bus Info Block Length	ROM Length	CRC	
Bus Info Block			1001
Root Directory			
Key	Entry_value		
			1002
Key	Entry_value		
Node Dependent Info Directory			1003
Instance Directory			1004
Root & Unit Leaves			1005
Unit Directory			1006
Vendor Dependent Information			1007
Key	Entry_value		

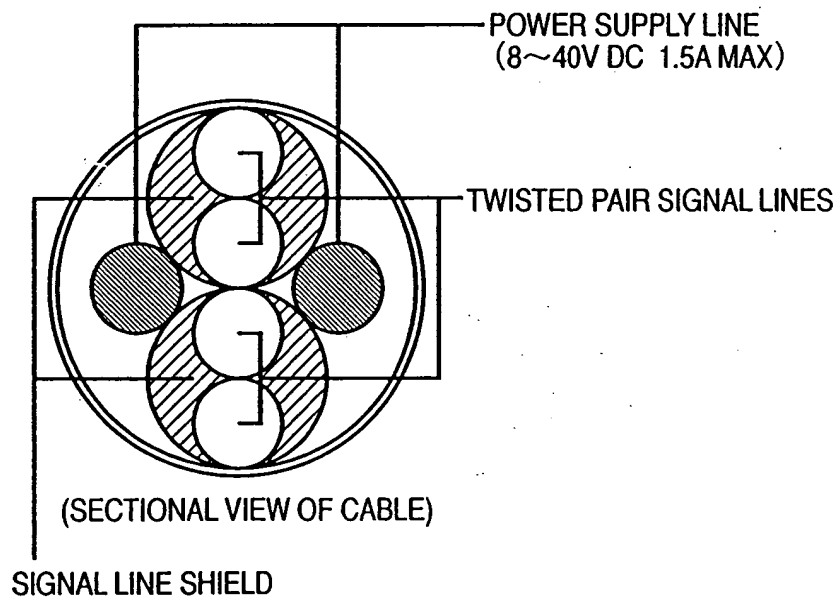
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FIG. 10

SERIAL BUS DEVICE REGISTER		
OFFSET (HEXADECIMAL)	REGISTER NAME	FUNCTION
800 } FFC		RESERVATION
1000 - } 13FC	TOPOLOGY_MAP	SERIAL BUS CONFIGURETION INFORMATION
1400 } 1FFC		RESERVATION
2000 } 2FFC	SPEED_MAP	SERIAL BUS TRANSFER RATE INFORMATION
3000 } FFFC		RESERVATION

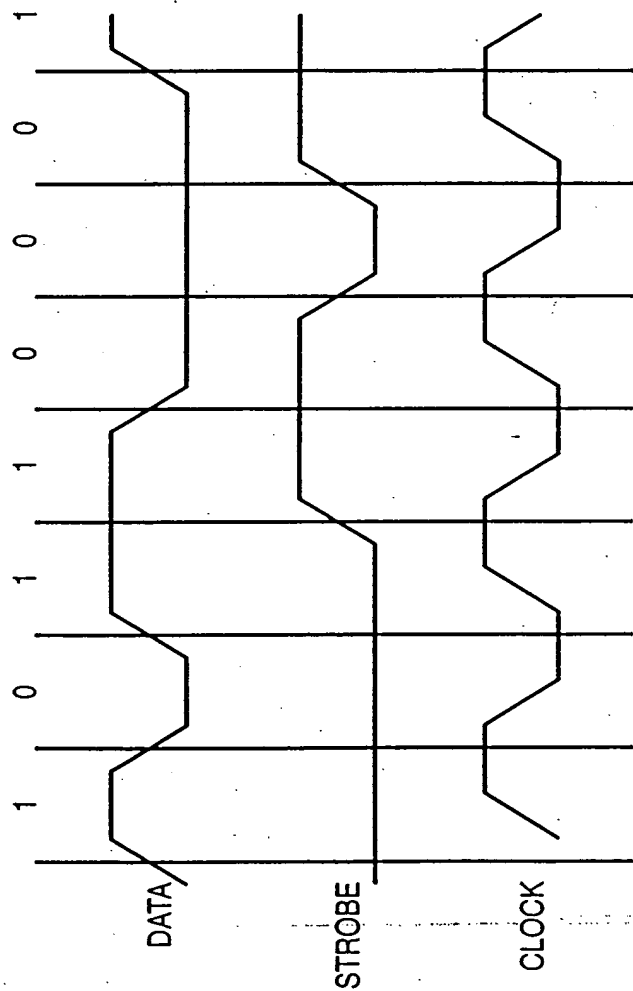
09939806-082801

FIG. 11



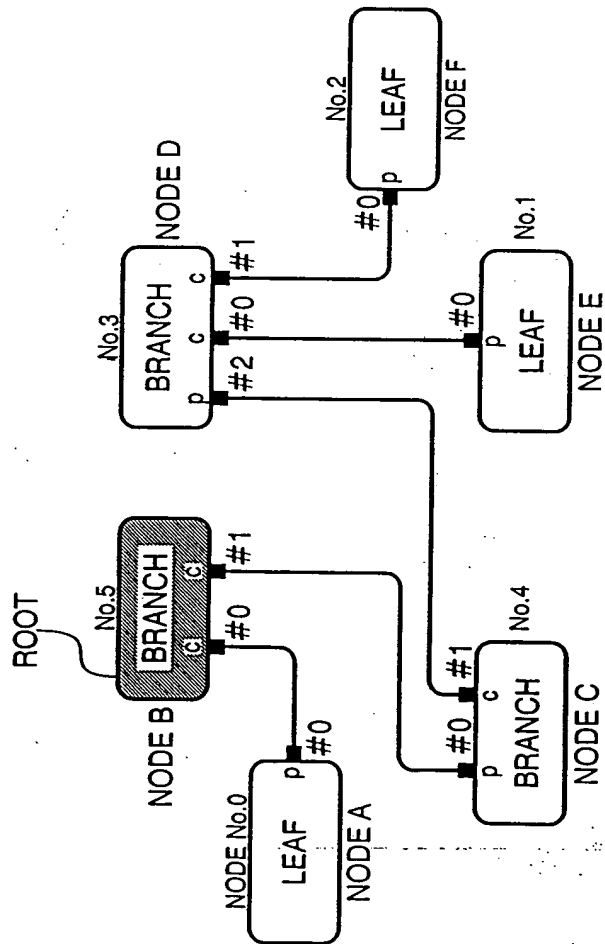
108280-9086E660

FIG. 12



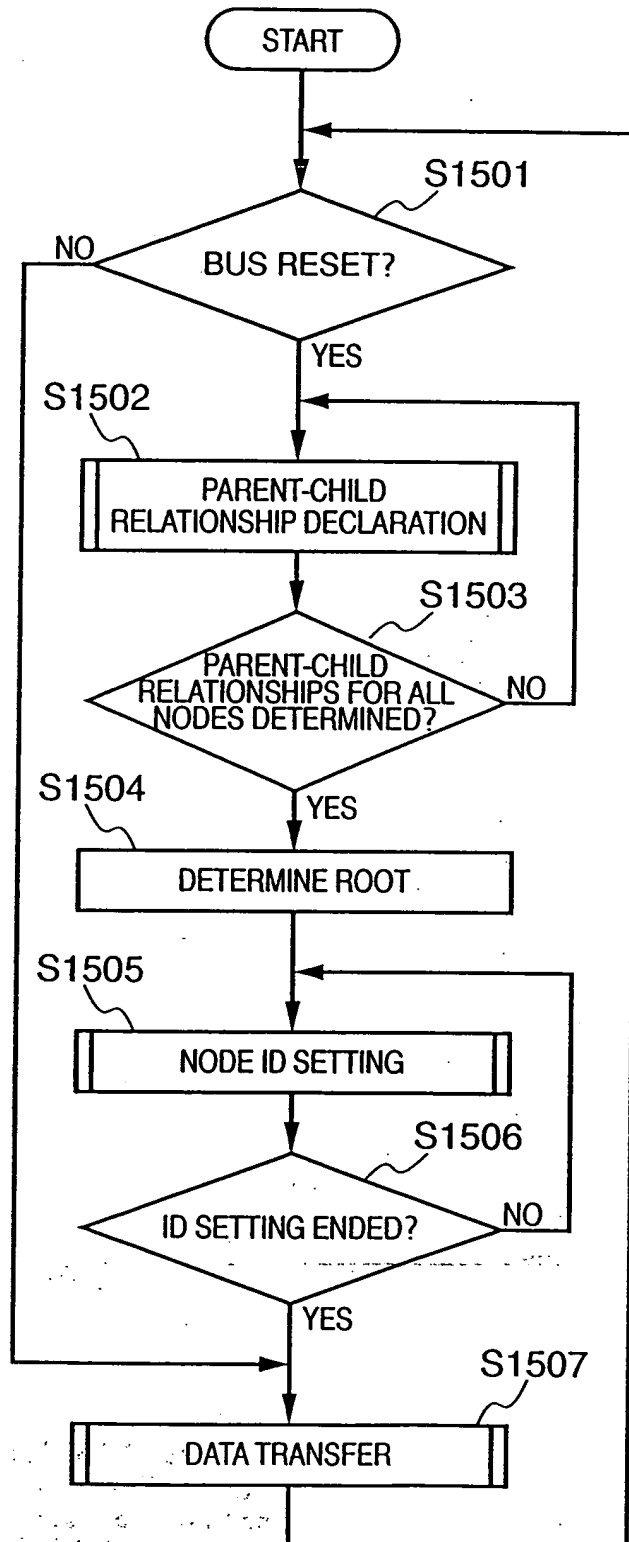
(CLOCK IS OBTAINED BY PERFORMING EXCLUSIVE OR DATA AND STROBE)

FIG. 13



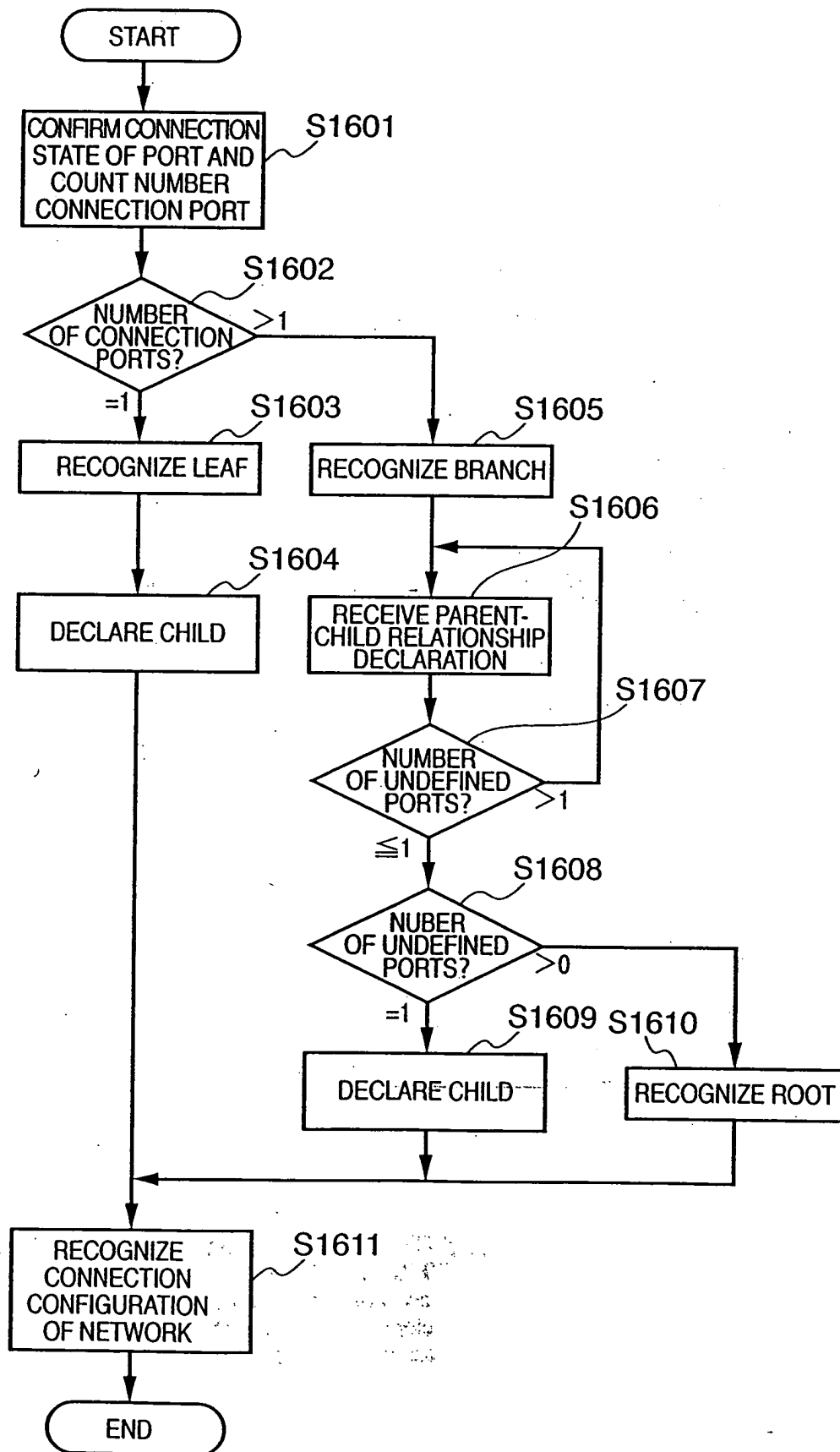
PORT
 p: PARENT PORT CONNECTED TO PARENT NODE
 c: CHILD PORT CONNECTED TO CHILD NODE

FIG. 14



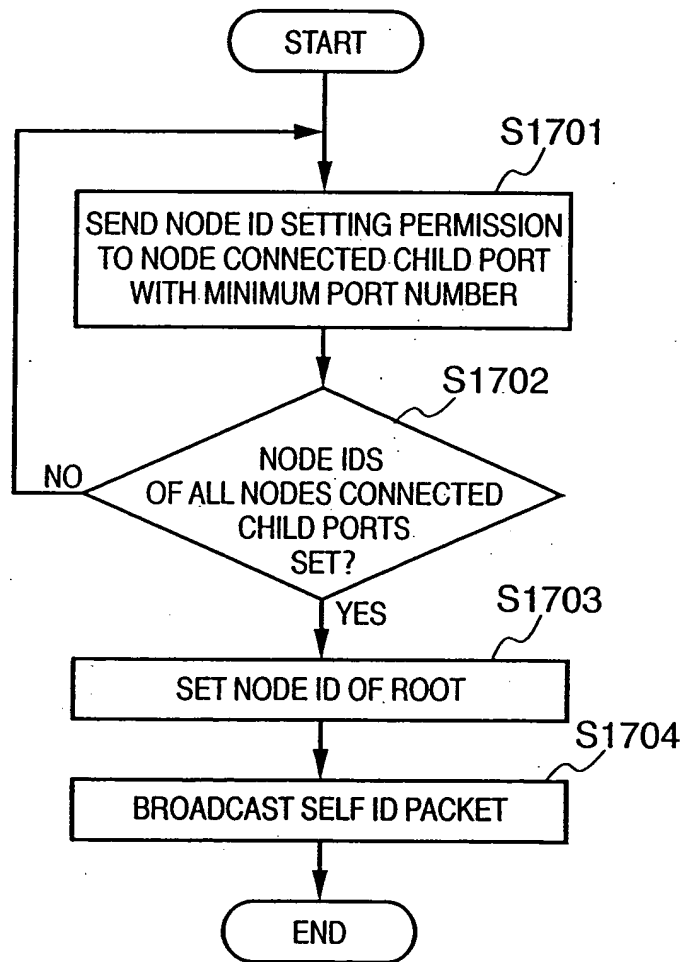
108280-9086660

FIG. 15



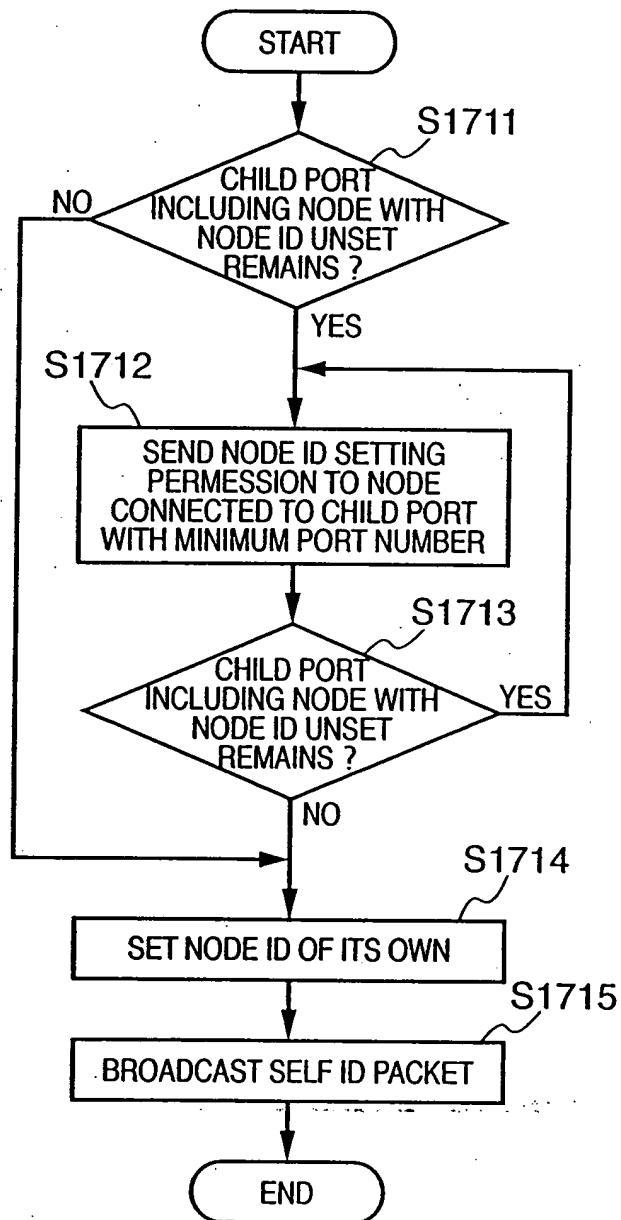
10939806-082801

FIG. 16



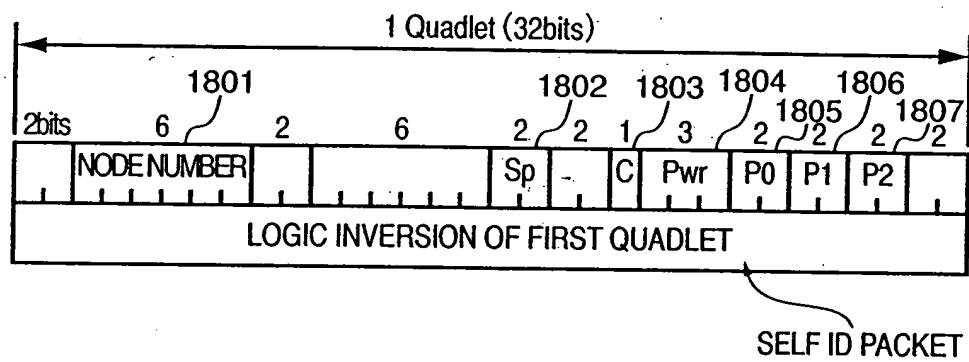
T08280-90866601

FIG. 17



F03280-9036E660

FIG. 18



09939806-082801

FIG. 19A

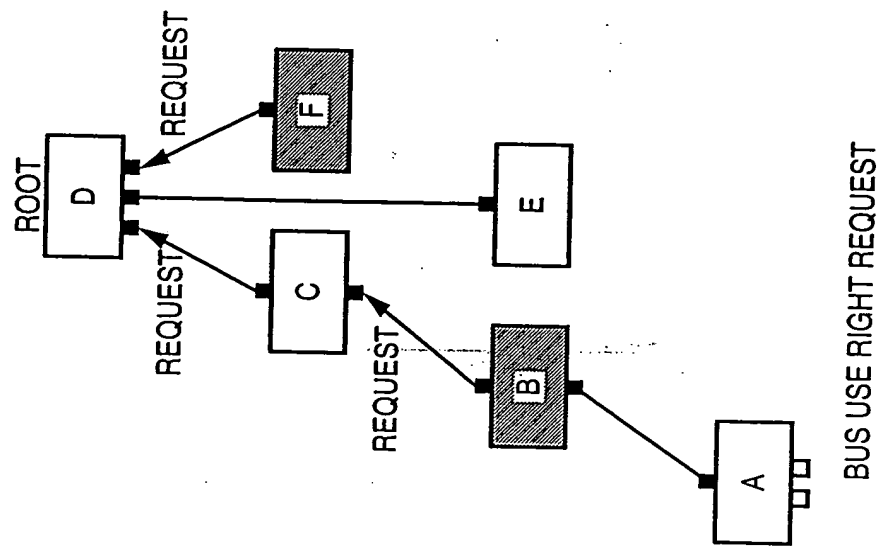


FIG. 19B

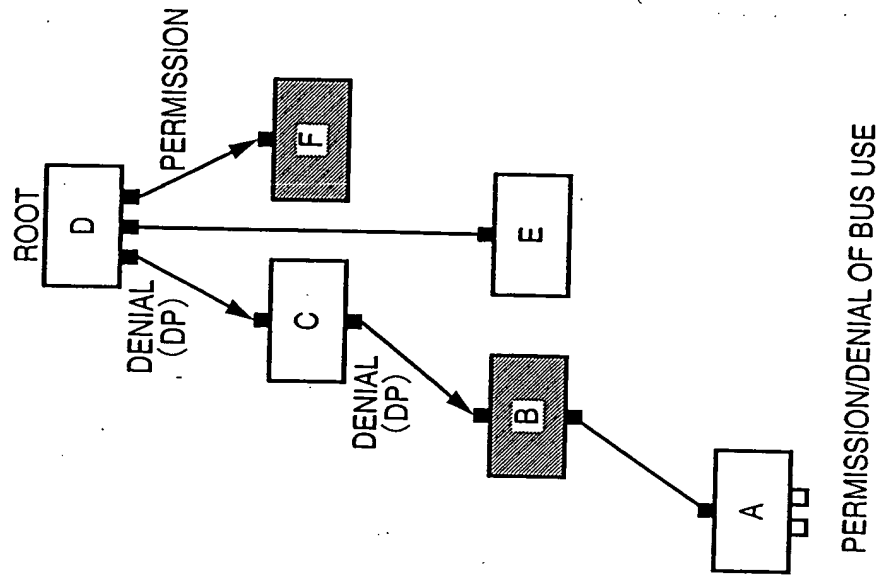


FIG. 20

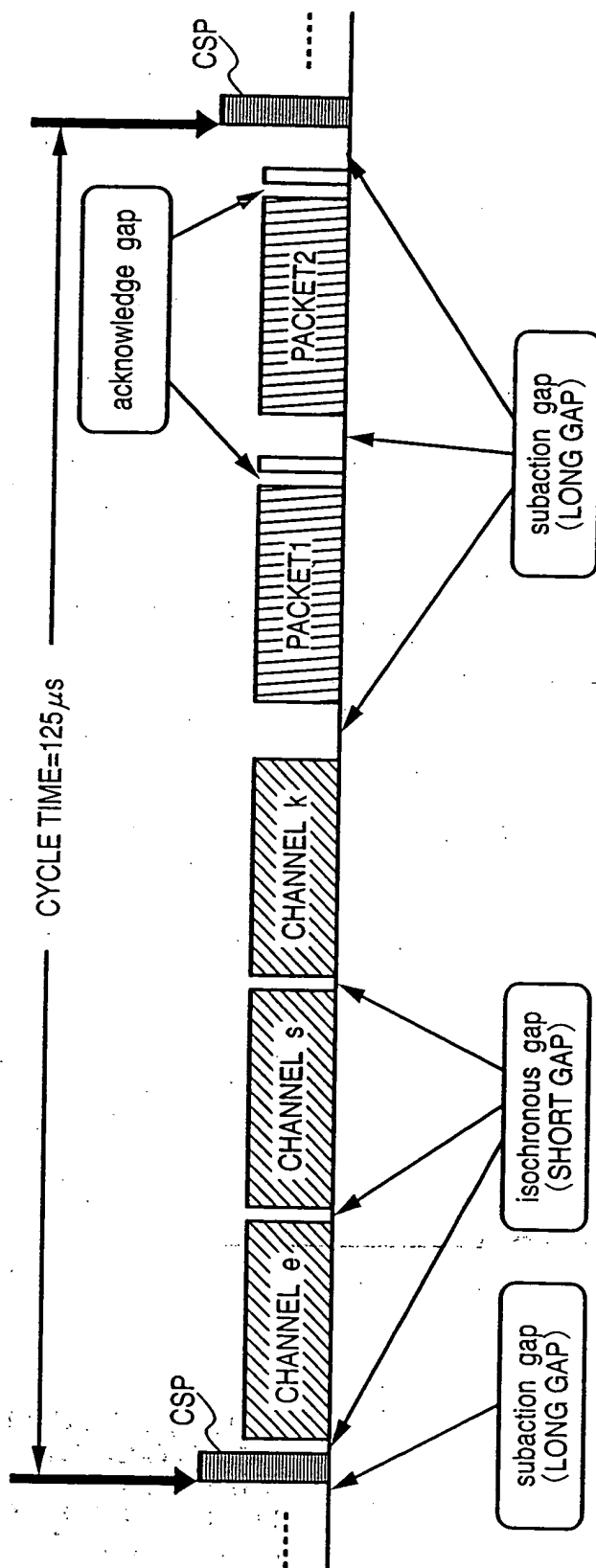


FIG. 21

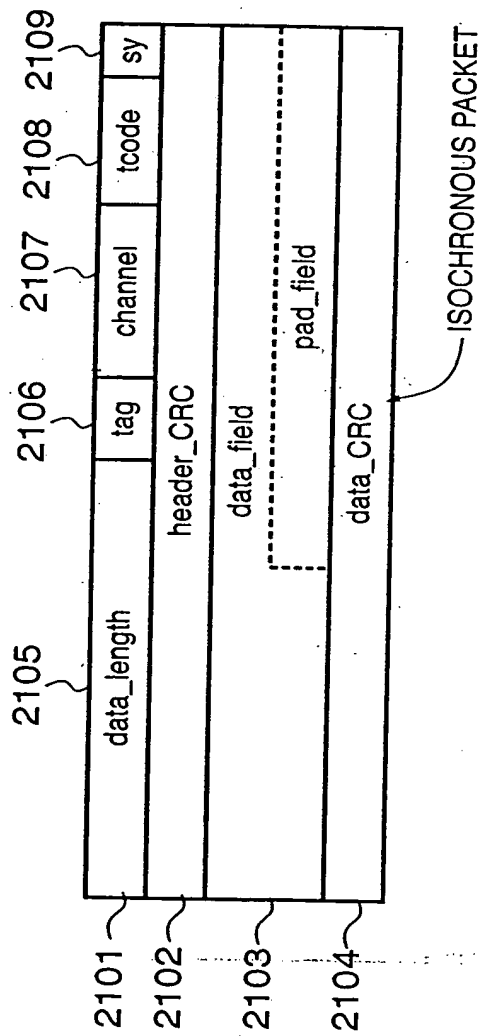


FIG. 22

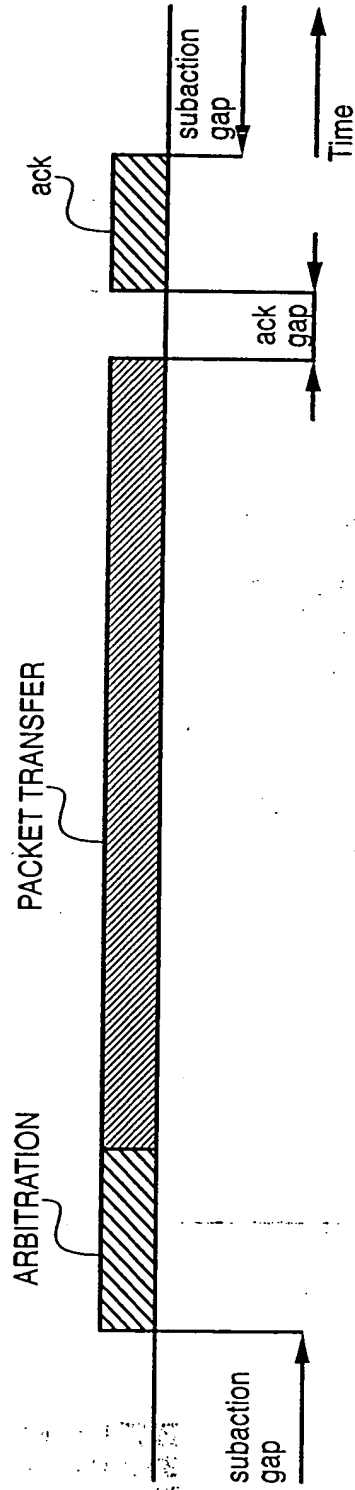


FIG. 23

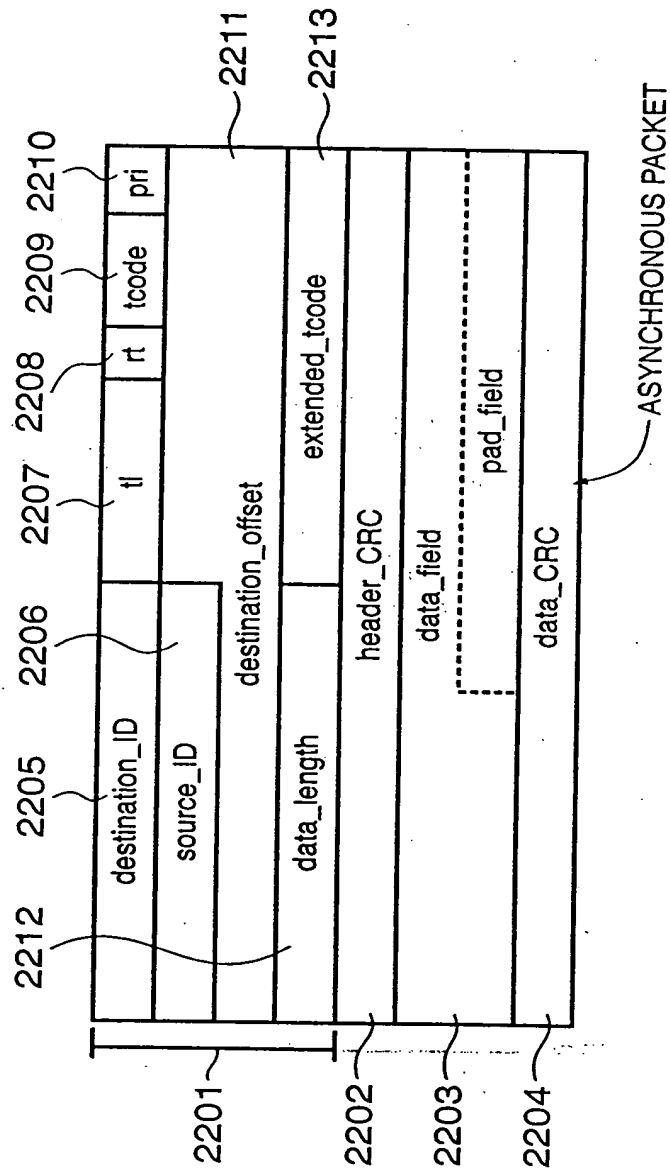


FIG. 24

ABBREVIATION	NAME	CONTENTS
destination_ID	destination identifier	INDICATING ID OF DESTINATION NODE (ONLY FOR ASYNCHRONOUS)
tl	transaction label	LABEL THAT INDICATES SERIES OF TRANSACTION (ONLY FOR ASYNCHRONOUS)
rt	retry code	CODE THAT INDICATES RE-SEND STATUS (ONLY FOR ASYNCHRONOUS)
tcode	transaction code	CODE THAT INDICATES TYPE OF PACKET (ONLY FOR ASYNCHRONOUS)
prt	priority	PRIORITY ORDER (ONLY FOR ASYNCHRONOUS)
source_ID	source identifier	INDICATION ID OF SOURCE NODE (ONLY FOR ASYNCHRONOUS)
destination_offset	destination memory address	MEMORY ADDRESS OF DESTINATION NODE (ONLY FOR ASYNCHRONOUS)
rcode	response code	RESPONSE STATUS (ONLY FOR ASYNCHRONOUS)
quadlet_data	quadlet (4bytes) data	4-BYTE LONG DATA (ONLY FOR ASYNCHRONOUS)
data_length	length of data	LENGTH OF data_field (EXCEPT pad bytes)
extended_tcode	extended transaction code	EXTENDED TRANSACTION CODE (ONLY FOR ASYNCHRONOUS)
channel	isochronous identifier	IDENTIFYING ISOCRONOUS PACKET
sy	synchronization code	USED TO SYNCHRONIZE VIDEO AND AUDIO (ONLY FOR ISOCRONOUS)
cycle_time_data	contents of the CYCLE_TIME register	VALUE OF CYCLE TIMER REGISTER OF CYCLE MASTER NODE (ONLY FOR CYCLE PACKET)
data_field	data +pad bytes	STORING DATA (ISOCRONOUS AND ASYNCHRONOUS)
header_CRC	CRC for header field	CRC FOR HEADER FIELD
data_CRC	CRC for data field	CRC FOR DATA FIELD
tag	tag label	LABEL INDICATING FORMAT OF ISOCRONOUS PACKET

00039806-082801

FIG. 25

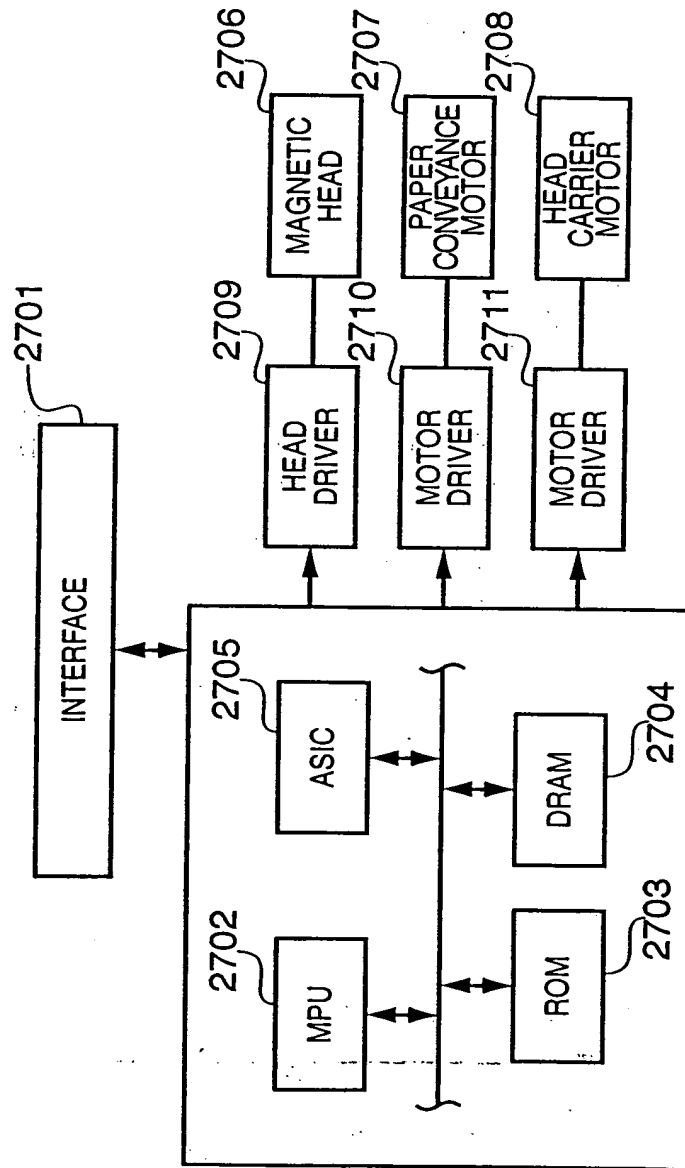


FIG. 26

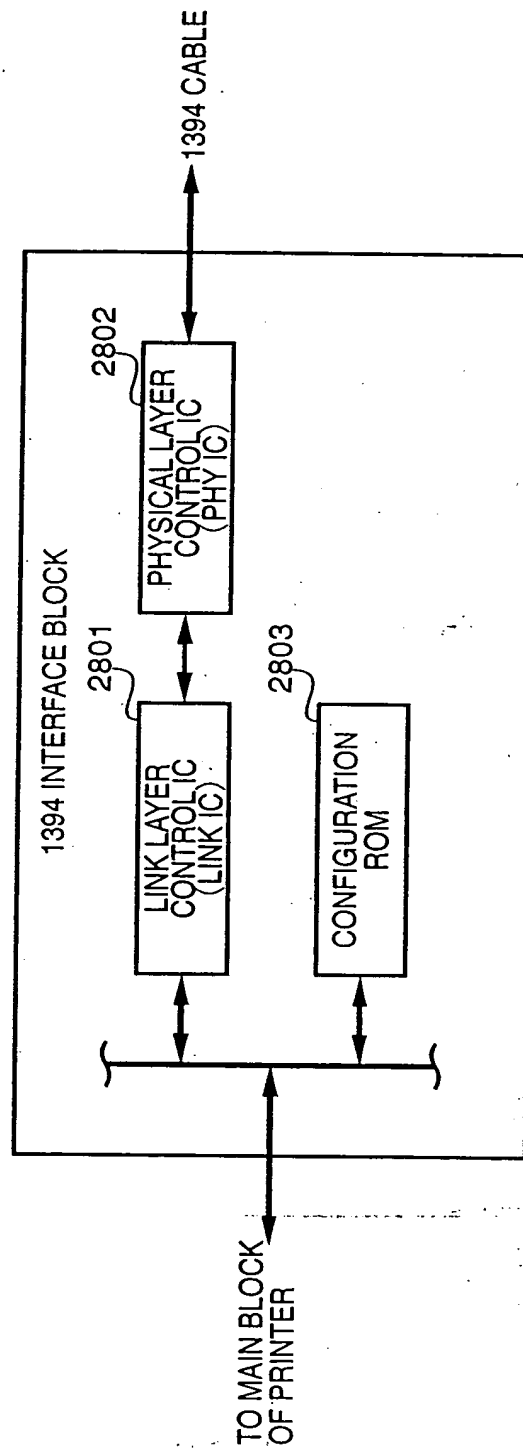
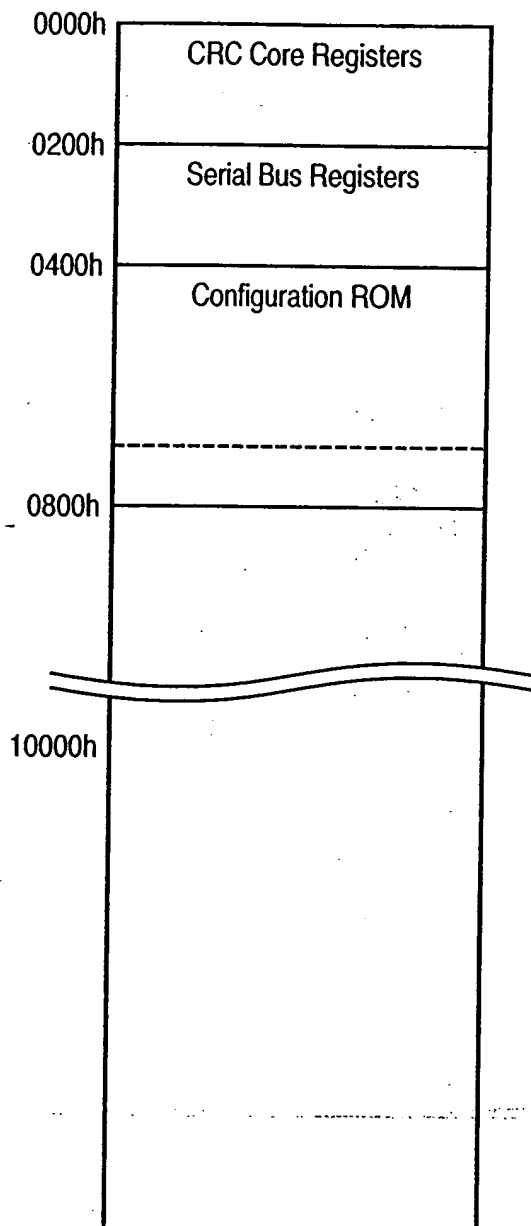


FIG. 27



00000000-00000000

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FIG. 28

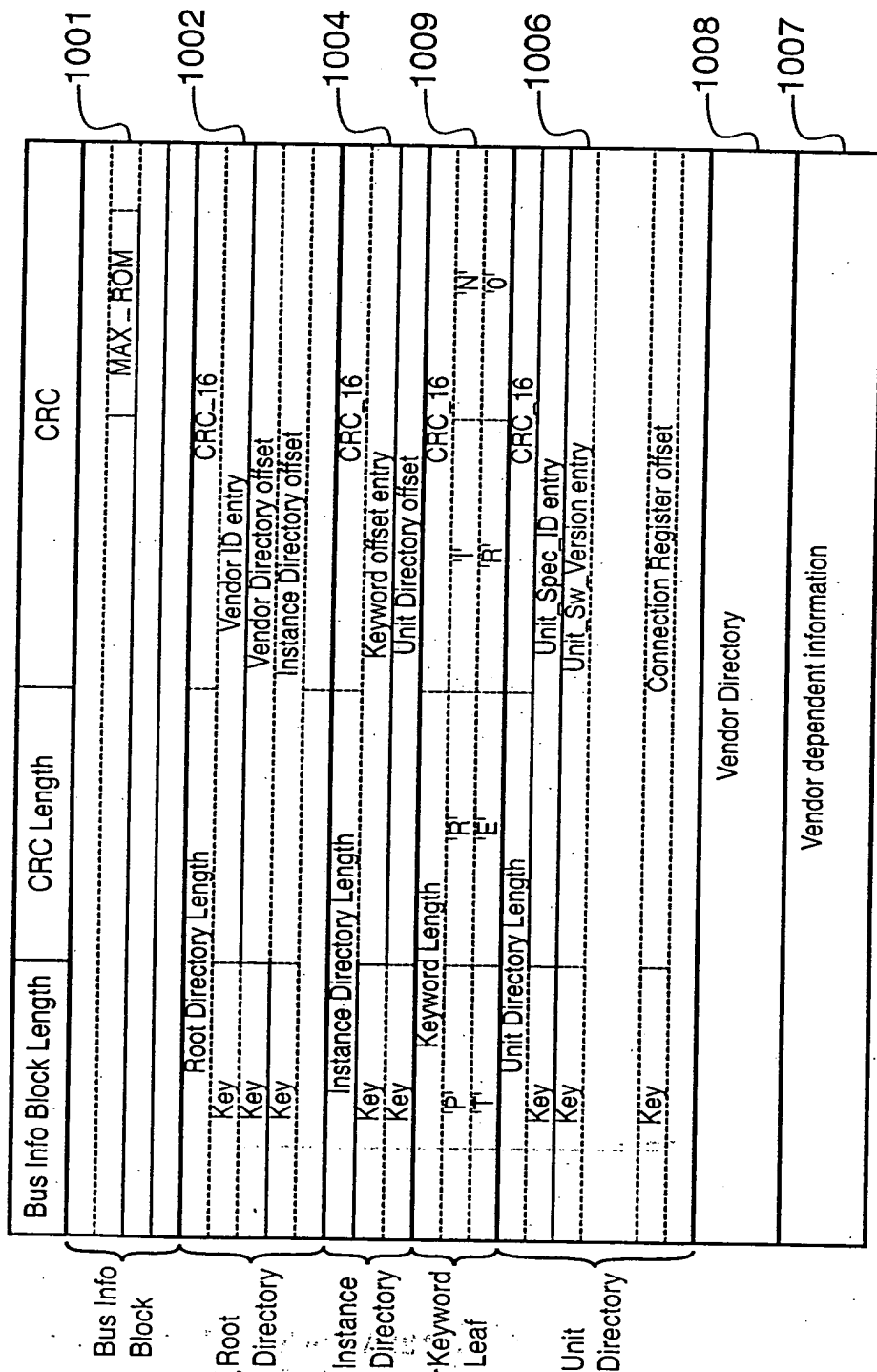
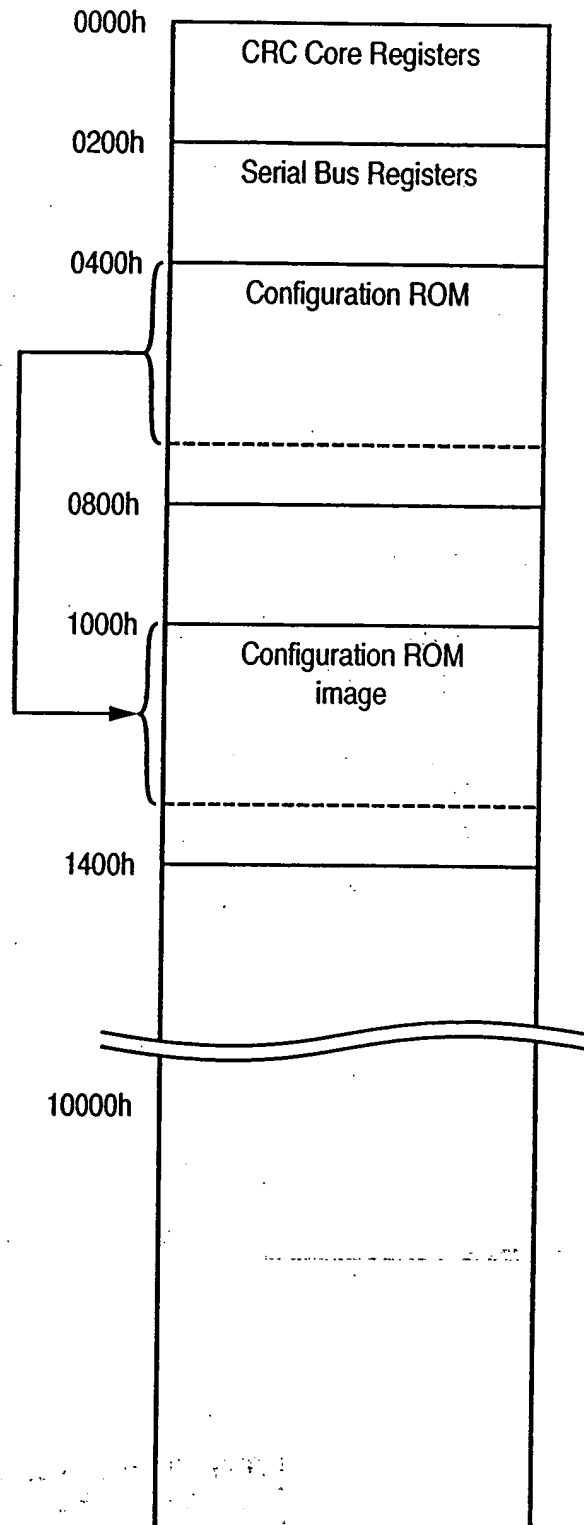


FIG. 29

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FIG. 30

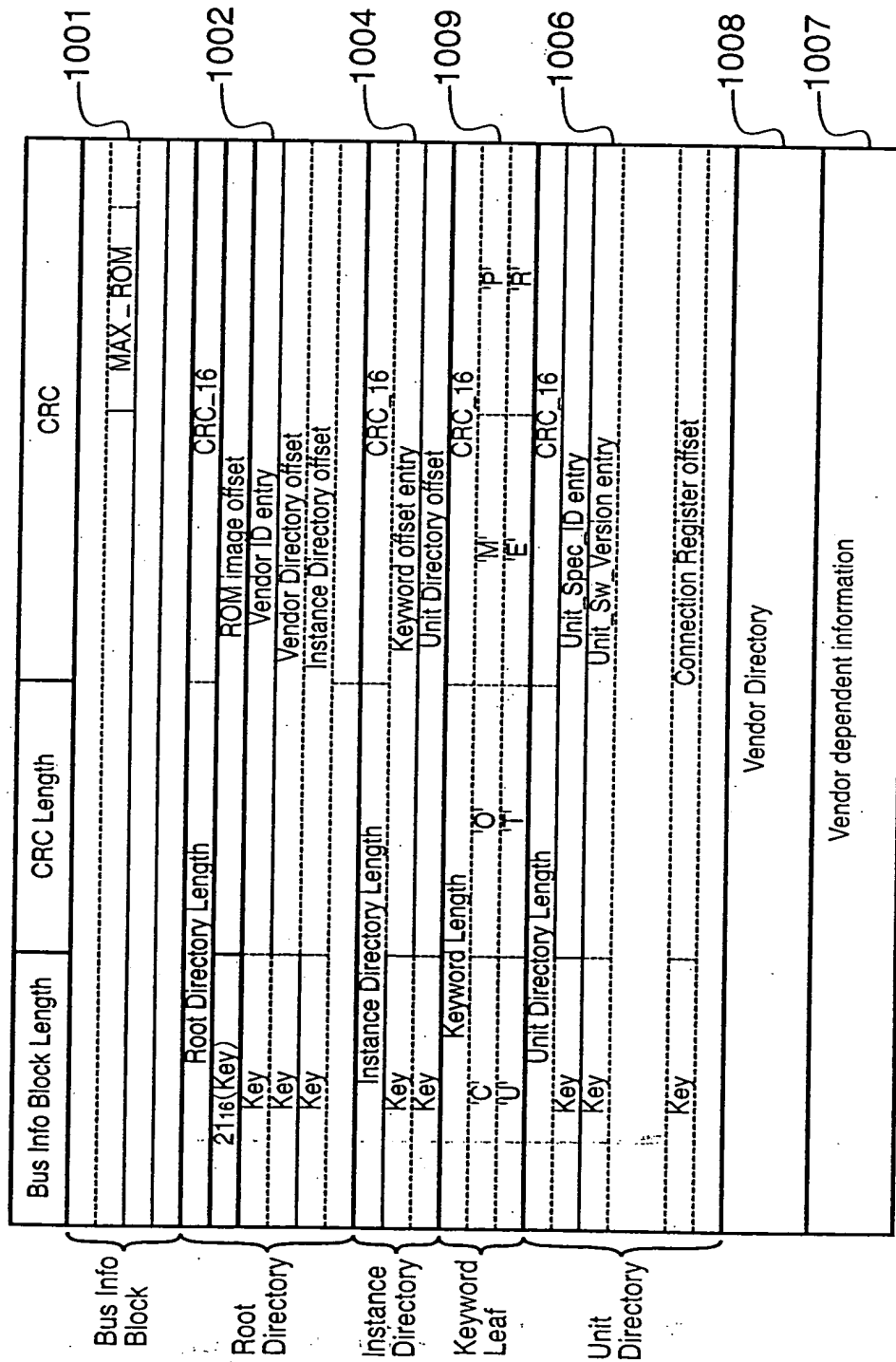
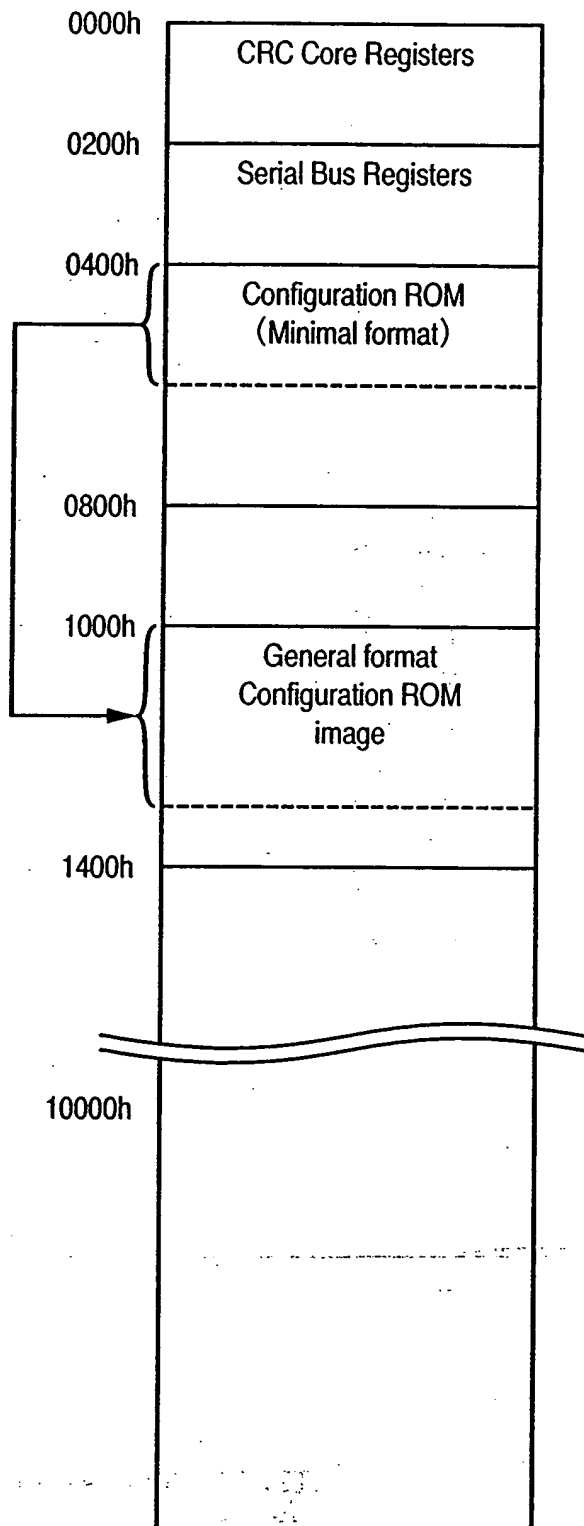


FIG. 31

T0B2B0" 90B6E660

FOB280* 9086E660

FIG. 32

1 ₁₆	Vendor_ID (rid)
38 ₁₆ (Key)	ROM image offset
Vendor Dependent Information	

FIG. 33

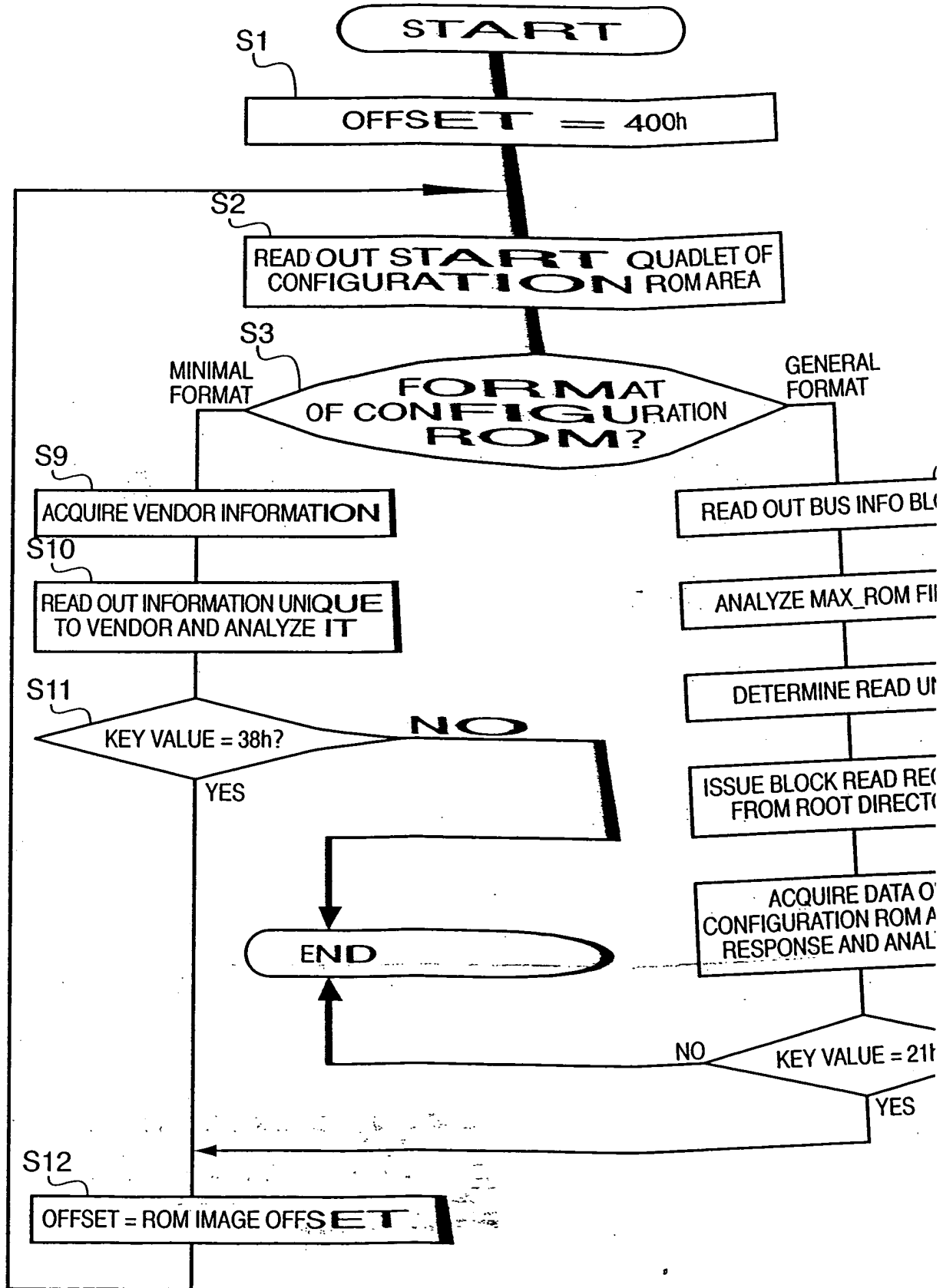
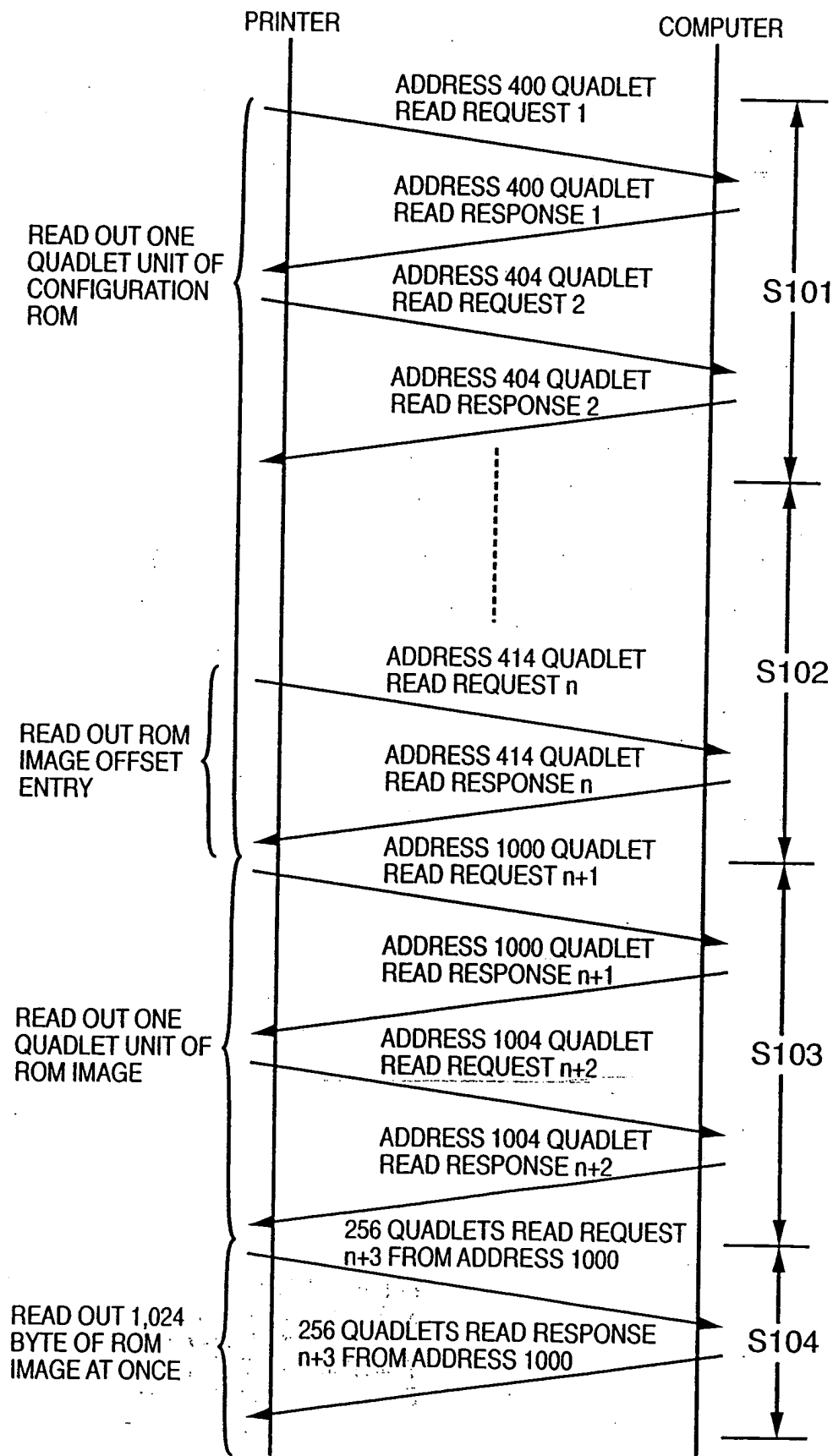


FIG. 34



T08280-9086660